

IIT Bombay Nanofabrication Facility Dept of Electrical Engg, Annexe IIT Bombay, Powai, Mumbai - 400076 (022)25764435



User Informa	ation				
Name of the u	ser:				
Email:					
Phone Numbe	r:				
Faculty/ Guide	Name:				
Faculty/ Guide	e Email:				
Name of the D	epartment:				
Sample Infor					
Substrate Size	:				
•	essing (For Fable le processed outs	•			
□ Yes □ No					
	•	abs outside IITBNF ed outside IITBNF	at any intermediate	e process step is	
Sample Histo	ory *				
Step	Process	Equipment Used	Equipment Category (Clean, Semi Clean, etc.)		Cleanliness of Lab
Number of Sar	mples:				

Process Description in detail * (Pls include resist details and recipe for litho)

We agree to acknowledge the Plasma Asher at IITBNF, Dept of Electrical Engineering,

Central Facility of IIT Bombay in our Publications/Reports/Thesis in which the data is used.

* Other details required would be communicated to the user depending on the submitted request.